

Fig. 1

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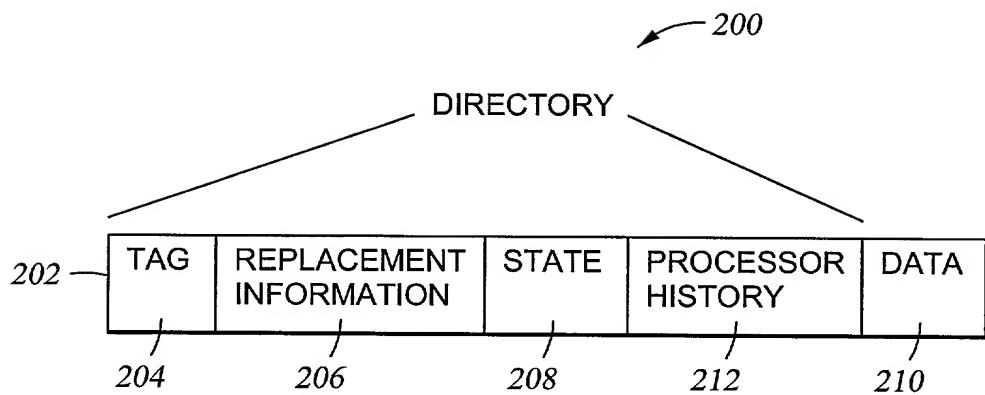


Fig. 2

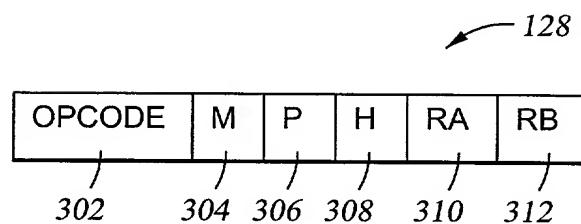


Fig. 3

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graph TD
    Start([START]) --> P1{P=1 ?}
    P1 -- NO --> M1{M=1 ?}
    M1 -- NO --> 402[402]
    M1 -- YES --> 404[404]
    M1 -- YES --> 404
    P1 -- YES --> 404
    404 --> End([USE HARDWARE TO DETERMINE  
PROCESSORS TO UPDATE])
  
```

